IN THE SPECIFICATION:

Kindly amend the specification as indicated herein.

In Page 5, line 3, after "the state from" replace ~~with~~ with --which-- as follows:

> ... The present invention uses a register to identify the state from ~~with~~ which a source operand is retrieved and to identify the state to which a destination operand is stored. In addition, the present invention includes an instruction that permits the setting of the source and destination thread back to the executing thread.

In Page 12, line 18, after "register" enter --(ROSR)--, in line 21, after "fetched 504" delete "in", and in line 22, after "program counter 512" insert --(PC)-- as follows:

> ... In one embodiment of the present invention, a read only status register (ROSR) that is associated with a particular thread (and is described below with reference to Figure 6) stores the context number. The next instruction in the thread is then fetched 504 ~~in~~ and the instruction address is decoded and stored in the program counter 512 (PC).

In Page 13, line 4, after "different" delete "a" as follows:

> ... In the present invention, one or more of these registers can be from a different ~~a~~ set of registers associated with a different context.

Also in Page 13, at line 20, after "use" insert --a-- as follows:

> ... If the instruction is part of a thread associated with a first context the present invention can use a register from a second context.